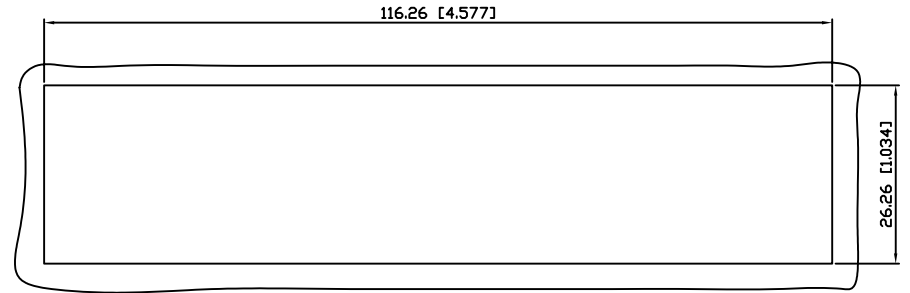
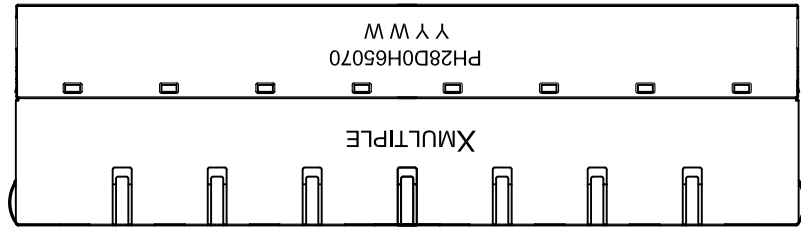
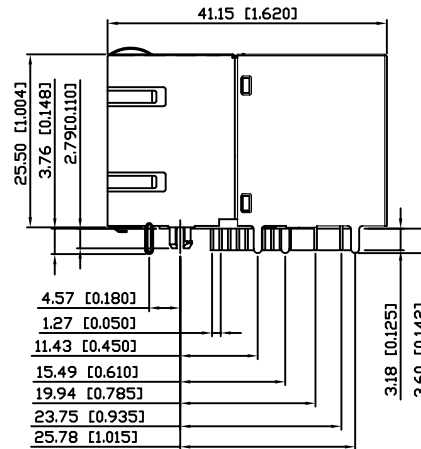
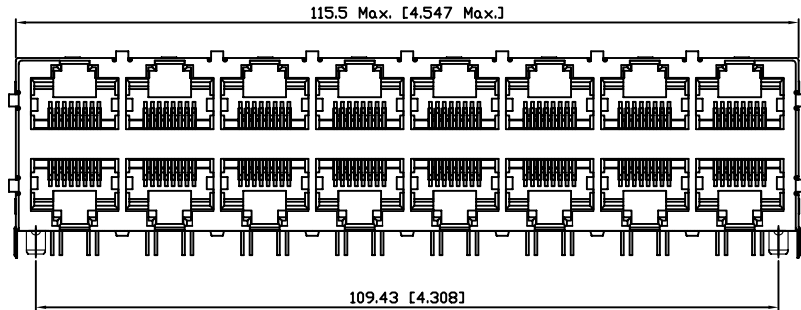


1. MECHANICAL DIMENSIONS :



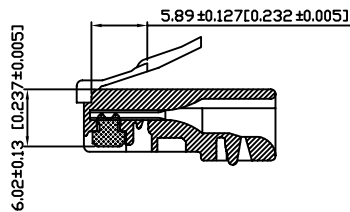
RECOMMENDED
PANEL CUTOUT DIMENSION



NOTES:

- MATERIAL:
HOUSING-PBT POLYESTER UL-94V-0
MIXED GLASS FIBER,
STANDARD COLOR-BLACK
CONTACTS-0.35mm THICK PHOS-BRONZE PLATED
WITH 30u" GOLD AND TIN IN
SOLDER AREA, AND 50u" NICKEL
PLATING IN ALL AREA,
SHIELD- SUS301 T=0.20 mm WITH TIN ON
TERMINATION END.
- CAVITY CONFORMS TO FCC RULES AND REGULATIONS
PART 68,SUBPART F.
- UNSPECIFIED TOLERANCE: .XX ±0.25[0.010]
X ±0.30[0.012]

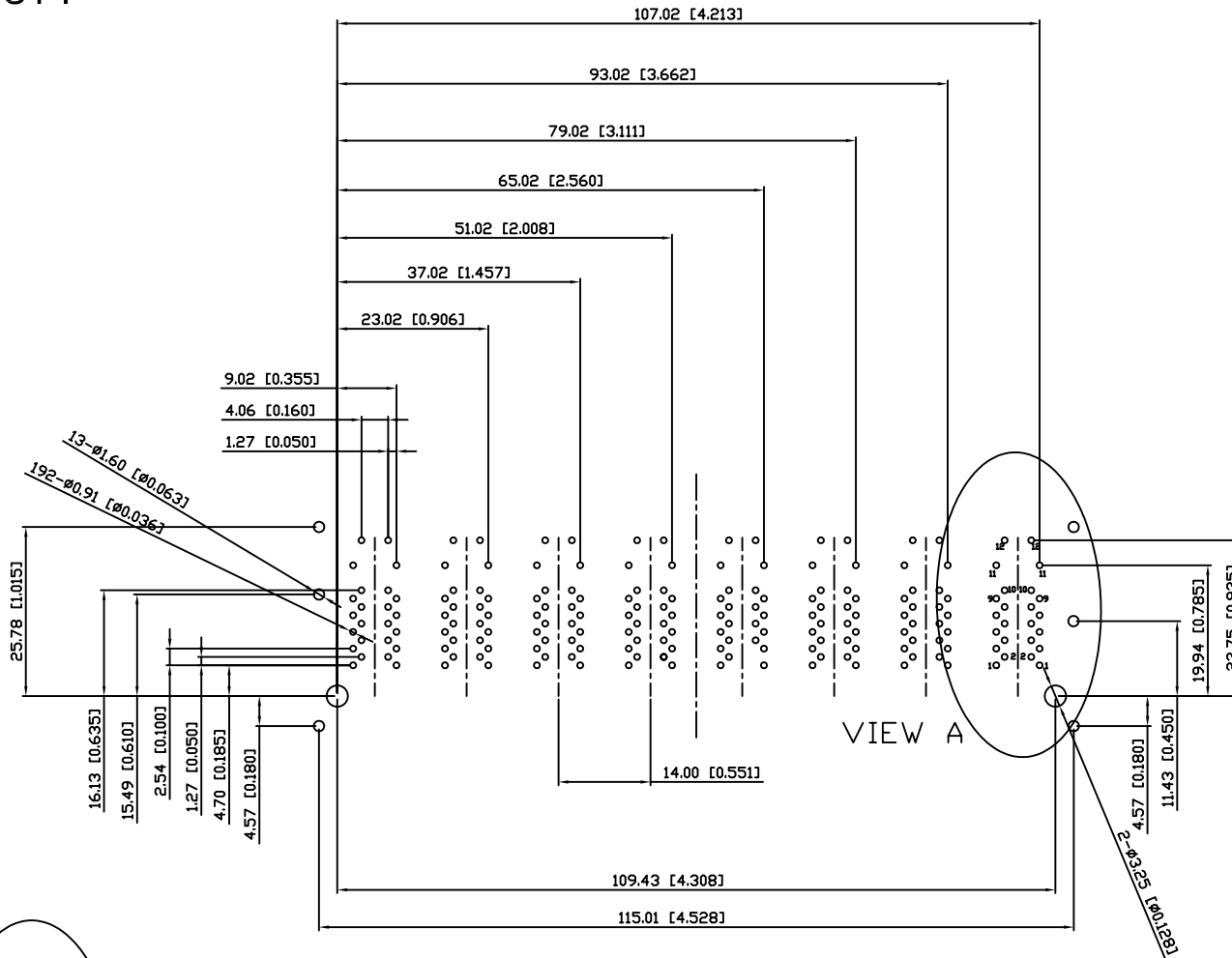
RECOMMENED PLUG DIMENSION:



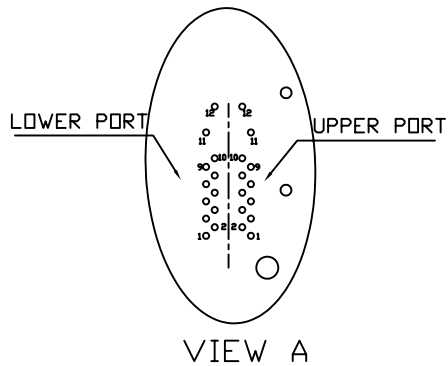
RoHS Compliant

<h1>X MULTIPLE</h1>		CONNECTING THE INFORMATION AGE	
X MULTIPLE ASIA 4F, No. 347, Yang Guang St., Neihu Chu, Taipei, Taiwan 11475		X MULTIPLE USA 1420 Los Angeles Ave., Suite G, Simi Valley, CA 93065	
THIS DRAWING IS A CONTROLLED DOCUMENT.		G/NO:	
TITLE: Stacked RJ45, W/D LED, with 10/100/1000 Mbps LAN Magnetics BP,10C, Shielded, Thru Hole, FDR PoE		DWG NO: XPJH-28D-0-H65-070	
PART NO: XPJH-28D-0-H65-070		DRAWN: AUSTIN	
		CHKD: JEFF	
		UNIT	SHEET
		mm [inch]	1 of 3
		REV	DATE:
		01	Jun.26.06

2. PCB LAYOUT :

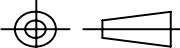


VIEW A

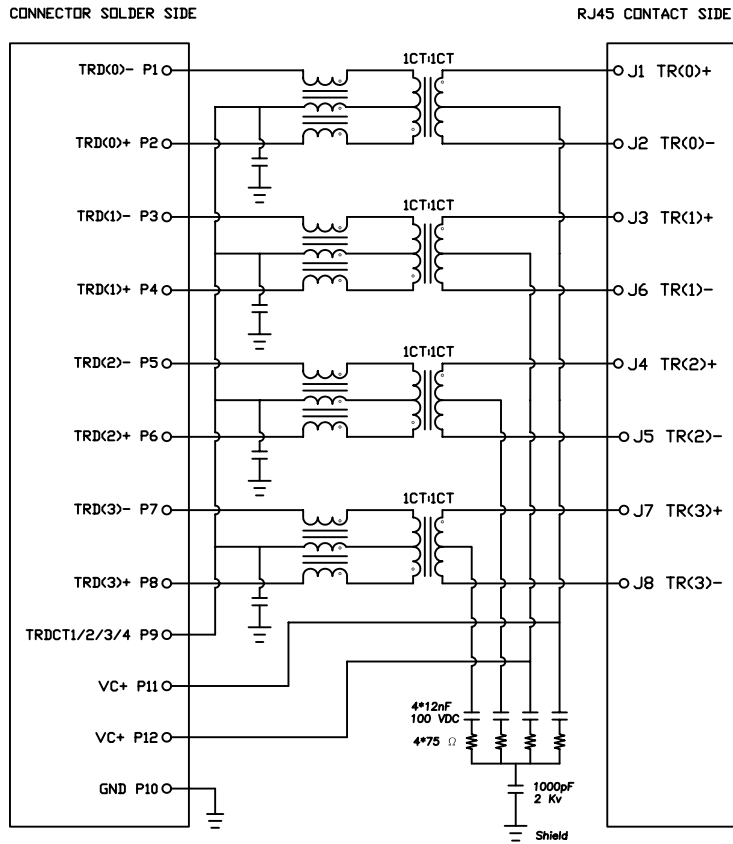


SUGGESTED PCB LAYOUT
(TOP VIEW)

RoHS Compliant

X MULTIPLE		CONNECTING THE INFORMATION AGE	
XMULTIPLE ASIA 4F, No. 347, Yang Guang St., Neihu Chu, Taipei, Taiwan 11475		XMULTIPLE USA 1420 Los Angeles Ave., Suite G, Simi Valley, CA 93065	
THIS DRAWING IS A CONTROLLED DOCUMENT.		G/ND:	
TITLE: Stacked RJ45, W/D LED, with 10/100/1000 Mbps LAN Magnetics BP,10C, Shielded, Thru Hole, FDR PoE		DWG NO: XPJH-28D-0-H65-070	
PART NO: XPJH-28D-0-H65-070		DRAWN: AUSTIN	
		CHKD: JEFF	DATE: Jun.26.06
		UNIT mm[inch]	SHEET 2 of 3

3. SCHEMATIC:



XPJH-28D-0-H65-070

PRODUCT SERIES
XPJH : TAB UP AND STACKED
FOR PoE TYPE

NUMBER OF PORTS
MECHANICAL TYPE
28D : 2*8 PORT, VERTICAL PIN,
LED ON TOP PORT ONLY OR
W/O LED

LED COLOR OPTION

PROTOCOL TYPE
H : 10/100/1000 Mbps, 8 CORE

CIRCUIT TYPE

SPECIAL CODE
0 : STANDARD

SHIELDED TYPE
7 : EMI FINGER ON ALL SIDE

LED POLARITY
0 : W/O LED

4. ELECTRICAL CHARACTERISTICS :

TEST NOTES:(25°±5°C)

- TR:(100KHz,0.1V); 100%
PINS:(P1,P2):(J1,J2)=1:1±3%;(P3,P4):(J3,J6)=1:1±3%
PINS:(P5,P6):(J4,J5)=1:1±3%;(P7,P8):(J7,J8)=1:1±3%
- LX:(100KHz,100mV,8mA, DC Bias) 100%
PINS:(P1,P2),(P3,P4),(P5,P6),(P7,P8)=350uH MINIMUM
- DCR: 100%
PINS:(P1,P2),(P3,P4),(P5,P6),(P7,P8)= 1.0 Ω MAXIMUM
PINS:(J1,J2),(J3,J6),(J4,J5),(J7,J8)=1.0 Ω MAXIMUM
- HIPOT: 100%
PINS(P1,P2)TO(J1,J2),(P3,P4)TO(J3,J6)=1650VAC FOR 2 SECONDS
PINS(P5,P6)TO(J4,J5),(P7,P8)TO(J7,J8)=1650VAC FOR 2 SECONDS
- INSERTION LOSS:
-0.8dB MAXIMUM AT 0.3MHz TO 100MHz;
-1.2dB MAXIMUM AT 100MHz TO 120MHz
- RETURN LOSS:
-18dB MINIMUM AT 0.5MHz TO 40MHz;
-12+20Log(f/80MHz)dB MINIMUM AT 40MHz TO 100MHz
- CROSS TALK:
-33+20Log(f/100MHz)dB MINIMUM AT 0.1MHz TO 100MHz
- COMMON TO DIFFERENTIAL MODE REJECTION:
-35dB MINIMUM AT 0.3MKHz TO 100MHz
- COMMON TO COMMON MODE REJECTION:
-30dB MINIMUM AT 0.3MHz TO 100MHz

RoHS Compliant

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THIS DRAWING IS A CONTROLLED DOCUMENT.		DWG NO: XPJH-28D-0-H65-070	
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PART NO: XPJH-28D-0-H65-070		CHKD: JEFF	
		UNIT	SHEET
		mm[inch]	3 of 3
		REV	DATE
		01	Jun.26.06