



SUGGESTED PCB LAYOUT  
(COMPONENT SIDE)

PCB LAYOUT  
TOLERANCE  
.XX ± 0.05 [0.002]

HOUSING: HIGH TEMP. THERMOPLASTIC, BLACK, UL 94V-0  
CONTACTS: 0.014 INCHES (0.35 MILLIMETERS) THICK PHOSPHOR BRONZE PLATED WITH 150 MICROINCHES (3.81 MICROMETERS) MINIMUM THICK TIN LEAD IN SOLDER AREA. 15 MICROINCHES (0.38 MICROMETERS) MINIMUM GOLD IN LOCALIZED PLATE AREA. ENTIRE CONTACTS PLATED WITH 50 MICROINCHES (1.27 MICROMETERS) MINIMUM THICK NICKEL.

- JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUBPART F
- PART NUMBER: XRJ-U-06-8-8-X-F1

REFER TO LED OPTIONS TABLE FOR ORDERING CODES

NOTES: UNLESS OTHERWISE SPECIFIED  
UNSPECIFIED TOLERANCE: .XX ± 0.25 [0.010]  
.X ± 0.30 [0.012]

<b>XMULTIPLE</b>		CONNECTING THE INFORMATION AGE
<small>XMULTIPLE ASIA 31F, No. 60, Puliwan St., Taipei, 104, Taiwan, R.O.C.</small>	<small>XMULTIPLE USA 8856 Old Converse Road Newbury Park, CA 91365</small>	<small>DRW: ONE NO: PDU08-802</small>
THIS DRAWING IS A CONTROLLED DOCUMENT.		
<small>TITLE: RJ45, Connectors, V/LED,SP,BC, Unshielded, Thru-Hole</small>	<small>PART NO: XRJM-U-08-8-8-X-F1</small>	<small>DRWR: VIQ</small>
	<small>DIMENSIONS mm[Inch]</small>	<small>REV J</small>
	<small>SHEET 1 of 1</small>	<small>DRW: C. TSN</small>
	<small>DATE 10/06/01</small>	