3. SCHMATIC:

CONNECTOR SOLDER SIDE

<table>
<thead>
<tr>
<th>TD+ P1</th>
<th>CT P2</th>
<th>TD- P3</th>
<th>RD+ P40</th>
<th>CT P5</th>
<th>RD- P60</th>
<th>P8</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>1CT: 1CT</td>
<td>1CT: 1CT</td>
<td>1CT:</td>
<td>1CT:</td>
<td>1CT:</td>
<td>1000 pF</td>
</tr>
<tr>
<td>TRANS</td>
<td></td>
<td></td>
<td>RECEIVE</td>
<td></td>
<td></td>
<td>2 Kv</td>
</tr>
<tr>
<td>75 OHM</td>
<td></td>
<td></td>
<td>75 OHM</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>O J1 TX+</td>
<td>O J4</td>
<td>O J2 TX-</td>
<td>O J3 RX+</td>
<td>O J7</td>
<td>O J8</td>
<td>O J6 RX-</td>
</tr>
</tbody>
</table>

RJ45 CONTACT SIDE

<table>
<thead>
<tr>
<th>O J3 TX+</th>
<th>O J4</th>
<th>O J2 TX-</th>
<th>O J3 RX+</th>
<th>O J7</th>
<th>O J8</th>
<th>O J6 RX-</th>
</tr>
</thead>
<tbody>
<tr>
<td>75 OHM</td>
<td></td>
<td></td>
<td>75 OHM</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

4. ELECTRICAL CHARACTERISTICS:

TEST NOTES: (25±5°C)

1. TR: (100kHz, 0.1V);
   - PINS: (P1-P3): (J1-J2): 1:1±3%
   - PINS: (P4-P6): (J3-J6): 1:1±3%

2. LX: (100kHz, 100mV, 8mA, DC Bias)
   - PINS: (P1,P3),(P4,P6)=350uH MINIMUM

3. DCR:
   - PINS: (J1-J2),(J3-J6)= 1.2 OHMS MAXIMUM

4. HIPO:
   - PINS(P1,P2,P3)TO(J1,J2)=1500VAC FOR 2 SECONDS
   - PINS(P4,P5,P6)TO(J3,J6)=1500VAC FOR 2 SECONDS

5. INSERTION LOSS:
   - -1.0dB MAXIMUM AT 1MHz TO 100MHz;

6. RETURN LOSS:
   - -20dB MINIMUM AT 1MHz TO 30MHz;
   - -15dB MINIMUM AT 30MHz TO 60MHz
   - -10dB MINIMUM AT 60MHz TO 80MHz

7. CROSS TALK:
   - -30dB MINIMUM AT 1MHz TO 100MHz

8. COMMON TO COMMON MODE REJECTION:
   - -30dB MINIMUM AT 1MHz TO 100MHz

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MULTIPLE CONNECTORS, W/LED, WITH 10/100/1000Mbps FILTER Shielded/Thru Hole

DRAWN BY: Jim Wang
CHECKED BY: Ann Yang
DATE: 2010.04.25
SIZE: A4 UNIT: mm
SCALE: 1:1 PROJECTION
REV.: A0
PART NO.: XMG-04G-1-D32-112
DWG NO.: LK-RJ-WST-085
SHEET 3 OF 3