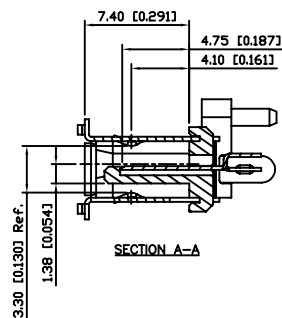
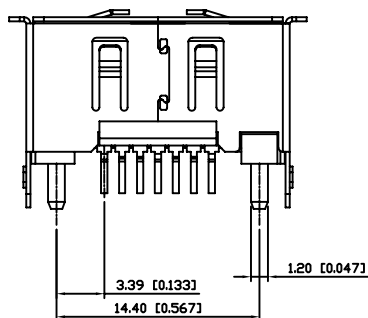


SUGGESTED PCB LAYOUT
TOP VIEW
DIMENSIONAL TOLERANCE: .XX ±0.08[0.003]



NOTE:

1. MATERIAL:
HOUSING: THERMOPLASTIC, HIGH TEMP, 30% G/F UL94 V-0
COLOR-WHITE
CONTACT: BRASS
SHELL: BRASS
2. FINISH:
CONTACT: 15u" GOLD/TIN PLATING ON CONTACT AREA
100u" MIN. TIN PLATING ON SOLDER TAILS
50u" MIN. NICKEL UNDERPLATING OVER ALL
SHELL: 50u" MIN. NICKEL UNDERPLATING OVER ALL
4. UNSPECIFIED TOLERANCE: .XX ±0.25[0.010]
X ±0.30[0.012]

RoHS Compliant

| | | | |
|--|---------------|--|-----------------|
| X MULTIPLE | | THE INFORMATION AGE | |
| MULTIPLE ASIA 4F, No. 347, Yang Guang St., Neihu, Taipei, Taiwan 11475 | | MULTIPLE USA 1420 Los Angeles Ave., Suite G, Simi Valley, CA 93065 | |
| THIS DRAWING IS A CONTROLLED DOCUMENT. | | G/NO: | |
| TITLE: E SATA 7PIN VERTICAL DIP | | DWG NO: XEST-07A2E3 | |
| PART NO: XEST-07A2E3 | | DRAWN: LEON | |
| | | CHKD: JEFF | |
| UNIT: mm[inch] | SHEET: 1 of 1 | REV: 01 | DATE: Mar.08.07 |