

SUGGESTED PCB LAYOUT TOP VIEW PCB LAYOUT TOLERANCE .XX ± 0.05 [0.002]

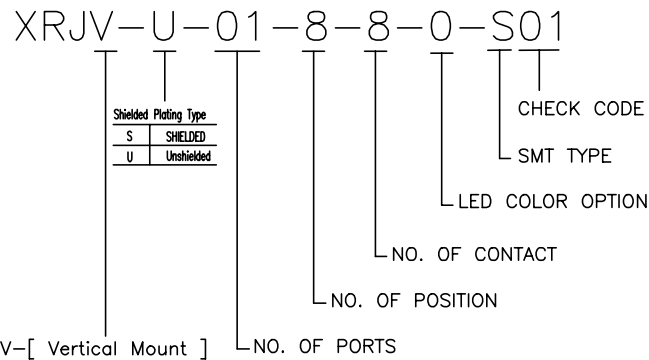
NOTES:

1. MATERIALS:

- HOUSING—DSM STANYL TE250F6 (NYLON-46)
HEAT DEFLECTION TEMP.290°
BLACK COLOR, UL 94V-0
- TERMINAL —0.35mm THICK PHOS-BRONZE
PLATED WITH 30u" GOLD AND
TIN/LEAD IN SOLDER AREA.
- HOLD DOWN—0.35mm THICK COPPER ALLOY,
PLATED WITH TIN-LEAD.

2. CAVITY CONFIRMS TO FCC RULES AND REGULATIONS PART 68,SUBPART F.

3. UNSPECIFIED TOLERANCE: .XX ± 0.25 [0.010]
.X ± 0.30 [0.012]



NOTE:

1. A AND B HIGHEST POINT MUST OVER C FLAT SURFACE.
2. THE MAXIMUM DISTANCE (0.15m/m) BETWEEN LOWEST AND HIGHEST ON A AND B FLAT SURFACE.

XMULTIPLE			CONNECTING THE INFORMATION AGE
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THIS DRAWING IS A CONTROLLED DOCUMENT.		CUSTOMER/NO:	
TITLE: RJ45 Jack, W/o LED, 8P, 8C, Unshielded, Vertical Mount, SMT		DWG NO: XRJV-U-01-8-8-0-S01	
PART NO: XRJV-U-01-8-8-0-S01		DRAWN: TARA	
		CHKD: JEFF	
	DIMENSIONS mm[inch]	SHEET 1 of 1	REV 01
			DATE: 30/07/04