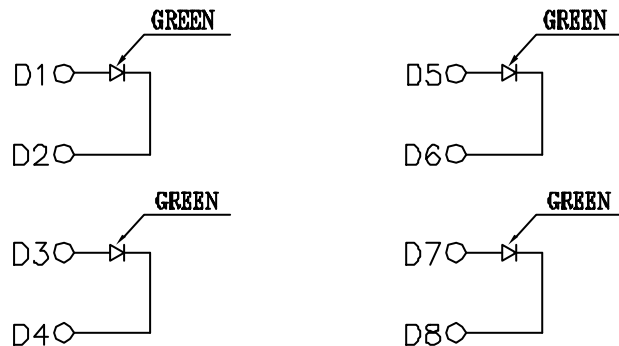
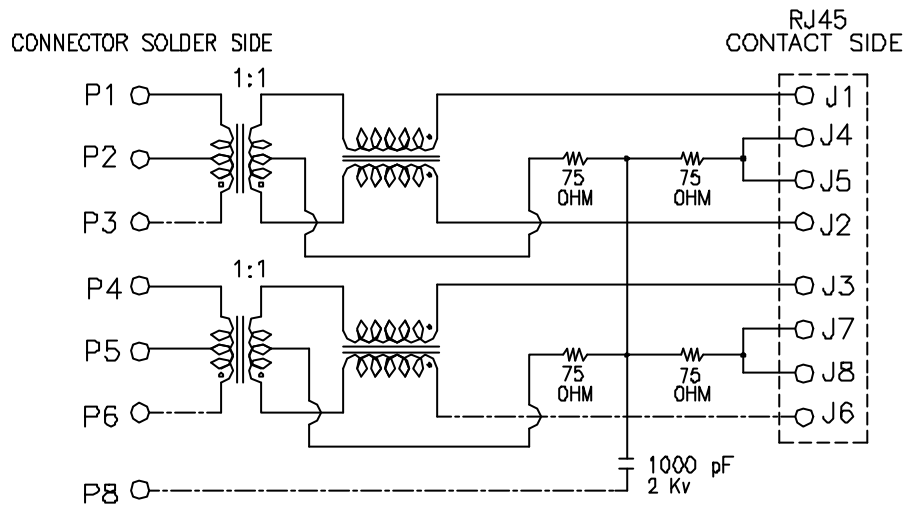


2. SCHEMATIC:



3. ELECTRICAL CHARACTERISTICS :

TEST NOTES:(25 ±5°C)

- 1.TR:(100KHz,0.1V);
PINS:(1-3):(J1-J2)=1:1±3%
PINS:(4-6):(J3-J6)=1:1±3%
- 2.LX:(100KHz,100mV,8mA, DC Bias)
PINS: (1,3),(4,6)=350uH MINIMUM
- 3.DCR:
PINS:(1-3),(J3-J6),(J1-J2)= 1.0 OHMS MAXIMUM
PINS:(4-6)=1.2 OHMS MAXIMUM
- 4.HIPOT:
PINS(1,2,3)TO(J1,J2)=1500VAC FOR 60 SECONDS
PINS(4,5,6)TO(J3,J6)=1500VAC FOR 60 SECONDS
- 5.CAPACITOR (1KHz,1V) :
SHIELD TO (J1-J8)=1300pF MAXIMUM AND 700pF MINIMUM
6. INSERTION LOSS:
-1.0dB MAXIMUM AT 0.1MHz TO 100MHz;
7. RETURN LOSS:
-20dB MINIMUM AT 1MHz TO 30MHz;
-15dB MINIMUM AT 30MHz TO 60MHz
-10dB MINIMUM AT 60MHz TO 80MHz
8. CROSS TALK:
-30dB MINIMUM AT 1MHz TO 100MHz
9. COMMON TO COMMON MODE REJECTION:
-30dB MINIMUM AT 1MHz TO 100MHz

XMULTIPLE		CONNECTING THE INFORMATION AGE	
XMULTIPLE ASIA 4F, No. 347, Jang Sung St., Nan-Shu City, Taipei, Taiwan 11176.		XMULTIPLE USA 643 Country Club Drive #8 12B Simi Valley, CA 93065	
THIS DRAWING IS A CONTROLLED DOCUMENT.		CUSTOMER/NO:	
TITLE: Stacked RJ45, W/LED, with 10/100 Mbps LAN Magnetics BP, BC, Unshielded, Thru Hole.		DWG NO: XRJ0-11-01-8-8-5-902	
PART NO: XRJG-11-01-8-8-5-902		DRAWN: TARA	
		CHKD: C. TSAI	
		DATE: 30/09/03	
DIMENSIONS mm[inch]		SHEET 2 of 2	
		REV 01	